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(CO2.

3.Sc. in CSE, 3rd Semester

ISLAMIC UNIVERSITY OF TECHNOLOGY (IUT) ORGANISATION OF ISLAMIC COOPERATION (OIC) DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING

Mid-Semester Examination

Winter Semester, A. Y. 2022-2023

October 11, 2023 (Afternoon)

e No.: EEE 4383 Time: 90 Min

Course Title: Electronic Devices and Circuits

Full Marks: 75

There are 3 (three) questions. Answer all 3 (three) questions. The symbols have their usual meanings. Programmable calculators are not allowed. Marks of each question and corresponding COs and POs are

written in the brackets.

I. a) Sketch the effect of an increasing level of R_C on the load line and the Q point in

Sector the effect of an increasing sevel of R_C on the load line and the Q point in output characteristics curve of BJT.

(CO1, PO1)

b) Write down BJT's different modes of operation and mention their corresponding | 05 | (COI, junction biasing with application.

For the network of Fig. 1(c1):

i. Sketch the load line on the characteristics for the transistor appearing in Fig. (C2).

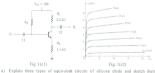
1(c2).

P

 For a Q-point at the intersection of the lead line with a base current of find the values of Located Verse

iii Determine the beta (6) at O-point

 Using the beta (β) for the network determined in part iii, calculate the required value of Rn.



corresponding V-I characteristics curve.

b) Show that the DC value of a full-wave restifier's output is approximately 0.636 times

the peak voltage, V_{rs}.

V_m=0.636 V_m

