M.Sc. TE. (2 yr), 1st Sem.

Date: October 09, 2023 (Afternoon)

ISLAMIC UNIVERSITY OF TECHNOLOGY (IUT) ORGANISATION OF ISLAMIC COOPERATION (OIC)

DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING

Mid-Semester Examination Course No.: EEE 6199 Course Title: Solid State Devices Winter Semester, A.Y. 2022-2023 Time: 90 Minutes Full Marks: 75

There are 4 (four) questions. Answer any 3 (three) questions. All questions carry equal marks. Marks in the margin indicate full marks. Do not write on this question paper.

- Briefly describe the Reverse Recovery Time for a diode.
 - b) With proper diagrams discuss about the different types of resistance levels considered 09 for p-n junction diode.
 - c) Describe the conditions established by zero bias, forward bias and reverse bias modes 10 of a p-n junction diode and how the resulting current is affected. (Draw necessary diagrams)
- a) In solid-state device fabrication, what is the purpose of Lithography? With proper 15 diagrams describe the steps of Lithography process needed to form the following pattern in the SiO₂ layer as shown in Fig. 1.



- b) What do you understand by dry and wet oxidation? For both dry and wet oxidation, 10 show the variation of oxide thickness with respect to oxidation time in case of (100) silicon surface on the same graph.
- a) With neat diagrams describe the steps involved in the fabrication of a p-n junction 15 diode.
 - b) With neat diagrams describe the construction and working principle of p-i-n 10 photodiode.
- 4. a) How can you categorize semiconductor photodevices? What are the different 05 categories?
 - b) Sketch the *I-V* characteristics of a *p-n* junction solar cell. Define what is meant by 10 short-circuit current, open circuit voltage and maximum power rectangle of a solar cell with necessary equations.
 - c) Write some applications of optoelectronic diodes. 0
 - d) What do you understand by spectral response and frequency response of an 05 optoelectronic device.