B.Sc. TE (2Yr) 1 st Semester	03 October 2023 (Morning)
ISLAMIC UNIVERSITY OF TECHNOLOGY (IUT) ORGANISATION OF ISLAMIC COOPERATION (OIC) Department of Computer Science and Engineering (CSE)	
MID SEMESTER EXAMINATION DURATION: 1 HOUR 30 MINUTES	WINTER SEMESTER, 2022-2023 FULL MARKS: 75
CSE 4573: Microprocessor at Programm	ning
Programmable calculators are not allowed. Do not write anything on the question paper. Assured 11.3 (three) questions. Figures in the right margin indicate full marks of questions.	

a) Differentiate between assembly language and machine language. How are these two re-

b) Write appropriate assembly language codes to accomplish the following tasks: i. 0Fh×(225-200)+127

ii. 0FFFh×10h+10101010b

c) Wtite an elaborative assembly language code for RET.

a) Write an assembly language program to allocate exactly 64 Kbytes of memory for code segment, 512 Bytes for stack segment and also consider that the size for data segment may exceed 64 Kbytes.

b) Suppose, Instruction Pointer (IP) of 8086 is moving in backward memory directions for executing the assembly language codes mentioned in Code Snippet 1:

1 MOV AX, FAIAh

2 NEG AX

Code Snippet 1: Assembly Language Code for Question 2.b)

After the executions of all arithmetic and logical instructions in single-step debug mode with disabling interrupt option, what values would be there in AX and Flag (CF, PF, AF, ZF, SF, OF, IF, TF, and DF) registers?

c) Differentiate between NEG and NOT instructions with example. a) Differentiate between the register sets of 8085 and 8086 microprocessors.

b) Derive the machine codes of the following instructions using their respective instruction format and also show how the machine codes of the instructions are to be stored in memory:

L IN AX, ABH H. MOV ES:[BX + A1B2H], DX

iii. MOV BX, 01010101B

c) In case of fetching a BYTE and WORD from the ODD and EVEN memory banks, how are the fetching cycles adjusted? Explain.