B.Sc. Engg. (EE), 1st Semester

ISLAMIC UNIVERSITY OF TECHNOLOGY (IUT) ORGANISATION OF ISLAMIC COOPERATION (OIC)

EEE

DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING

Semester Final Examination	Winter Semester, A. Y. 2022-2023
Course No.: EEE 4101	Time: 3 Hours Full Marks: 150
Course Title: Electrical Circuit I	Full Marks: 150

There are 6 (six) questions. Answer all the questions. Marks are given in the right margin (in brackets) along with course outcome and program outcome. Assume any data if necessary.

1(a) Find the value of V₀ in the network in Fig. 1 (a) without using any network analysis (12.5) technique. (CO1) (PO1)

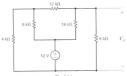


Fig. 1 (a)

1(b) Use nodal analysis to find V0 in the network in Fig. 1 (b).

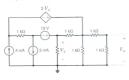
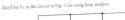


Fig. 1 (b)



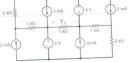


Fig. 2 (a)

2(b) Use superposition theorem to find I₁ in the actwork in Fig. 2 (b).

Fig. 2 (b)



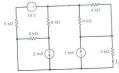


Fig. 3 (a)

(12.5) (CO2) (PO2)

(CO2) (PO2)

(12.5) (CO1) (PO1) 3(b) Mention the difference between diamagnetic, paramagnetic and ferromagnetic. (12.5) Explain the term gauss and permittivity. Find the energy stored by the inductor in the (CO3) circuit in Fig. 3 (b) when the current through it has reached its final value. (PO1)



Fig. 3 (b)

4(a) Find the magnetic flux Φ established in the series magnetic circuit in Fig. 4 (a).

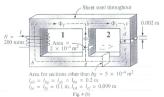
(CO5)



$$l_{ab}^{cu} = l_{bc} = l_{cf} = l_{fa} = 0.2 \text{ m}$$

Area (throughout) = 2 × 10⁻⁴ m²
 $l_{bc} = l_{dc}$
Fig. 4 (a)

4(b) For the series-parallel magnetic circuit in Fig. 4 (b), find the value of I required to (establish a flux in the gap of $\Phi_g = 2.5 \times 10^{-4}$ Wb.



5(a) For the circuit in Fig. 5 (a):	(12.5)
i. Find the total admittance and impedance in polar form.	(CO4)
ii. Sketch the admittance and impedance diagrams.	(PO2)
iii. Find the value of C in microfarads and L in henries.	
iv. Find the voltage E and currents I_R , I_L , and I_C in phasor form.	

v. Sketch the phasor diagram of the currents Is, Ig. Ig. and Ic. and the voltage E.

vi. Verify Kirchhoff's current law at one node.

vii. Find the average power delivered to the circuit.

viii. Find the power factor of the circuit, and indicate whether it is leading or lagging.

ix. Find the sinusoidal expressions for the currents and voltage.

x. Sketch the waveforms for the currents and voltage on the same set of axes.

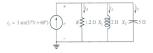


Fig. 5 (a)

(CO4)

5(b) For the network in Fig. 5 (b):

i. Find E. Le, and L in phasor form.

ii. Find the total power factor, and indicate whether it is leading or lagging.

iii. Find the average power delivered to the circuit.

iv. Sketch the admittance diagram.

v. Sketch the phasor diagram of the currents Ir, In and IL and the voltage E.

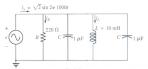
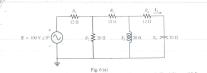


Fig. 5 (b)

6(a) Find the current I₄ for the network in Fig. 6 (a). Also find the voltage across all the (12.5) elements. (CO4)



6(b) Using mesh analysis, find the current through the capacitive reactance in Fig. 6 (b).

(PO2)

