May 24, 2024

B.Sc. in EEE, 6th Sem, B.Sc.TE 2nd Sem, B.Sc.TE 4th Sem

ISLAMIC UNIVERSITY OF TECHNOLOGY (IUT) ORGANISATION OF ISLAMIC COOPERATION (OIC) DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING

Semester Final Examination Course No.: EEE 4605 / EEE 4691 Course Title: Control System Engineering I Summer Semester, A. Y. 2022-2023 Time: 3 Hours Full Marks: 150

The symbols have their usual meanings. Programmable calculators are not allowed. Marks of each question and corresponding COs and POs are written in the brackets.

$$G(s) = \frac{K(s + 5)}{s(s^2 + 4s + 25)}$$

- Design a cascade compensator to maintain overshoot of 15% by reducing the settling time by three times. In addition, reduce the steady-state error by 10 times. Generate a table to compare the results of uncompensated and compensated systems. (40)
- ii) Sketch the physical realization of the designed compensator using active networks. (10)
- iii) Design a feedback compensator using 2^{sd} approach of the feedback compensation. Use overshoot of 10% in the minor loop, and overshoot of 15% in the major loop. Generate a table to compare the results of uncompensated and compensated systems. (30)
- iv) Sketch the Bode plot and the Nyquist diagram of the system. (40)
- v) Find the gain margin, phase margin, zero dB frequency, 1800 frequency, and the closed-loop bandwidth. Hence estimate the damping ratio, percent overshoot, settling time, and peak time. And find the steady-state error from the bode plot. (30)